# NATIONAL <br> SENIOR CERTIFICATE 

## GRADE 12

## SEPTEMBER 2023

## ELECTRICAL TECHNOLOGY: DIGITAL ELECTRONICS MARKING GUIDELINE

MARKS: 200

This marking guideline consists of 14 pages.

## INSTRUCTIONS TO MARKERS

1. All calculations with multiple answers imply that any relevant, acceptable answer should be considered.
2. Calculations:
2.1 All calculations must show the formulae.
2.2 Substitution of values must be done correctly.
2.3 All answers MUST contain the correct unit to be considered.
2.4 Alternative methods must be considered, provided that the correct answer is obtained.
2.5 Where an incorrect answer could be carried over to the next step, the first answer will be deemed incorrect. However, should the incorrect answer be carried over correctly, the marker has to re-calculate the values, using the incorrect answer from the first calculation. If correctly used, the candidate should receive the full marks for subsequent calculations.
2.6 Markers should consider that learners answers may deviate slightly from the marking guideline depending on how and where in the calculation rounding off was used.
3. This marking guideline is only a guide with model answers.
4. Alternative interpretations must be considered and marked on merit. However, this principle should be applied consistently throughout the marking session at ALL marking centres.

## QUESTION 1: MULTIPLE-CHOICE QUESTIONS

| 1.1 | $\mathrm{~B} \checkmark$ | $(1)$ |
| :--- | :--- | :--- |
| 1.2 | $\mathrm{~A} \checkmark$ | $(1)$ |
| 1.3 | $\mathrm{C} \checkmark$ | $(1)$ |
| 1.4 | $\mathrm{D} \checkmark$ | $(1)$ |
| 1.5 | $\mathrm{D} \checkmark$ | $(1)$ |
| 1.6 | $\mathrm{~B} \checkmark$ | $(1)$ |
| 1.7 | $\mathrm{D} \checkmark$ | $(1)$ |
| 1.8 | $\mathrm{~B} \checkmark$ | $(1)$ |
| 1.9 | $\mathrm{D} \checkmark$ | $(1)$ |
| 1.10 | $\mathrm{D} \checkmark$ | $(1)$ |
| 1.11 | $\mathrm{D} \checkmark$ | $(1)$ |
| 1.12 | $\mathrm{~B} \checkmark$ | $(1)$ |
| 1.13 | $\mathrm{~A} \checkmark$ | $(1)$ |
| 1.14 | $\mathrm{D} \checkmark$ | (15) |
| 1.15 | $\mathrm{C} \checkmark$ | $(1)$ |

## QUESTION 2: OCCUPATIONAL HEALTH AND SAFETY (GENERIC)

2.1 - Integrity $\checkmark$

- Sense of responsibility $\checkmark$
- Emphasis on quality
- Discipline
- Sense of teamwork
2.2 An event that causes grave or severe physical injury to a person, $\checkmark$ threatening their health and safety.
2.3 - Avoid direct contact with any chemical $\checkmark$
- Always wear personal protective clothing
- Always read and be aware of the warning symbols on containers
(Any $1 \times 1$ )
2.4 An unsafe act is the wilful performance of a task or activity in a manner that may threaten the health and safety of everyone.
A calculated risk is the probability that injury or damage may occur while using dangerous equipment.
2.5 - To prevent oneself from being electrocuted.
- To prevent injury
(Any $1 \times 1$ )
2.6 This could damage the equipment and render the equipment unsafe $\checkmark$ and compromise the safety of the user.


## QUESTION 3: SWITCHING CIRCUITS

3.1 3.1.1 Monostable multivibrator $\checkmark$
3.1.2 Bistable multivibrator $\checkmark$
3.2

3.3 $\quad$ 3.3.1 Bistable $\checkmark$ multivibrator $\checkmark$
3.3.2 Resistors $R_{1}$ and $R_{2}$ are pull-up $\checkmark$ resistors.
3.3.3 When set is pressed, it pulls pin 2 'low' $\checkmark(0 \mathrm{~V})$ and cause the output to go 'high'. $\checkmark$ turning the LED on. $\checkmark$
3.3.4 Threshold pin 6 is purposefully held at $0 \vee \checkmark$ causing the IC not to reset, $\checkmark$ keeping the output high $\checkmark$ when $S_{1}$ is pressed.
3.4 3.4.1 The output signal represents an inverting Schmitt trigger $\checkmark$ because the output signal is inverted $\checkmark$ with reference to the input signal.
3.4.2

3.5 3.5.1 The gain of the amplifier is determined by the ratio $\checkmark$ of the feedback resistance $\checkmark$ to the input resistance of each.
3.5.2 Vout $=-\left(V_{1} \frac{R f}{R 1}+V_{2} \frac{R f}{R 2}+V_{3} \frac{R f}{R 3}\right) \checkmark$

$$
\begin{align*}
& =-\left(200 \mathrm{mV} \frac{100 \mathrm{k} \Omega}{20 \mathrm{k} \Omega}+300 \mathrm{mV} \frac{100 \mathrm{k} \Omega}{10 \mathrm{k} \Omega}+400 \mathrm{mV} \frac{100 \mathrm{k} \Omega}{25 \mathrm{k} \Omega}\right) \\
& =-(200 \mathrm{mV} \times 5)+(300 \mathrm{mV} \times 10)+(400 \mathrm{mV} \times 4) \checkmark \\
& =-5,6 \mathrm{~V} \checkmark \tag{4}
\end{align*}
$$

3.5.3 $\mathrm{V}_{\text {OUT }} \quad=\mathrm{V}_{\text {IN }} \times$ Gain

$$
\begin{align*}
\text { Gain } A v & =\frac{V_{\text {OUT }}}{V_{\text {IN }}} \quad \checkmark \\
& =\frac{5,6}{0,2+0,3+0,4} \checkmark \\
& =6,2 \mathrm{~V} \checkmark \tag{3}
\end{align*}
$$

3.5.4 With a variable resistor in the feedback loop, the gain $\checkmark$ of the amplifier can be varied / controlled.
3.5.5 If $R_{2}$ is changed to $5 \mathrm{k} \Omega$, the gain for $\mathrm{V}_{2}$ will increase, $\checkmark$ causing the total output voltage to increase.
3.6


(6)
3.7 - The inputs draw zero current.

- The two inputs will always have the same voltage.
- The capacitor will charge at a constant rate, when a constant current is supplied.


## QUESTION 4: SEMICONDUCTORS

4.1


Drawing correct
Inputs and Output labelled correct.
Positive and Negative supplies labelled correct.
4.2.1 The signal will be 10 times greater than the input signal/the output will be 1 V . $\checkmark$ and the output signal will be $180^{\circ}$ out of phase with the input signal. $\checkmark$
4.2.2 The signal will be 11 times greater than the input signal/the output will be $1,1 \mathrm{~V}$ and the output signal will be in phase with the input signal.
4.3.1 No, the output signal is incorrect. $\checkmark$ The output signal should be $180^{\circ}$ out of phase with the input signal because the circuit is an inverting amplifier.
4.3.2 $\quad \mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {IN }}\left(\frac{\mathrm{R}_{\mathrm{f}}}{\mathrm{R}_{\text {IN }}}\right) \vee \checkmark$
$3 V=20 \times 10^{-3}\left(\frac{150 \times 10^{3}}{R_{\text {IN }}}\right) V \checkmark$
$R_{\mathrm{IN}}=1000 \Omega$
$R_{\text {IN }}=1 \mathrm{k} \Omega \checkmark$
4.4.1 Trigger $\checkmark$
4.4.2 - Pin 6 sets the voltage at which the 555 IC will trigger.

- It is used to maintain the voltage across the timing capacitor $\checkmark$ which is then discharged through Pin 7. $\checkmark$
4.4.3 The 555 IC can only operate at power supply voltages between $+5 \mathrm{~V} \checkmark$ to +18 V .
4.4.4 The RS flip-flop stores the incoming information temporarily, $\checkmark$ until new information is received.


## QUESTION 5: DIGITAL AND SEQUENTIAL DEVICES

5.1 - Liquid crystal is placed between two transparent plates.

- The transparent plates are polarised.
- A reflecting metal surface is mounted behind the lower plate
- A transparent plastic plate is placed on top of the upper polarised plate. $\checkmark$
5.2 - In common anode $\checkmark$ all eight LEDs are internally connected together to a common positive voltage rail.
- In common cathode $\checkmark$ all eight LEDs are internally connected to a common 0 V ground. d. $\checkmark$
5.3 - Input voltage: 2 V (2,1 to 2,5 may also be taken as correct) $\checkmark$
- Current flow: 20 mA
- Reverse voltage: 6 V
- Power: 600 mW
- Soldering time: 5 s
$5.4 \quad 5.4 .1$

5.4.2 $W=0 \checkmark$
$X=1 \checkmark$
$Y=0 \checkmark$
$Z=0 \checkmark$
5.5



## NOTE:

1 mark for each logic gate.
A mark is allocated to a logic function only if the relevant function's inputs and outputs are connected correctly and labelled correctly where applicable.
$5.6 \quad 5.6 .1$


NOTE:
1 mark for each NAND gate $=2$
1 mark for each NOR gate = 2
1 mark for the NOT gate
1 mark for the latch = 1

### 5.6.2

| INPUTS |  | OUTPUTS |  |
| :---: | :---: | :---: | :---: |
| CLK | $\mathbf{D}$ | $\mathbf{Q}$ | $\mathbf{Q}$ |
| 0 | 0 | LATCH |  |
| 0 | 1 | LATCH (UNCHANGED) |  |
| $\mathbf{1}$ | 0 | 0 | 1 |
| 1 | 1 | 1 | 0 |$\checkmark \checkmark \checkmark$

1 mark for each correct row on the table
$5.7 \quad 5.7 .1$


NOTE: A mark is allocated for the correct count at each clock pulse e.g. the count at clock pulse 3 should be 011 and at clock pulse 8 the count should be 000 .
5.7.2 The circuit in FIGURE 5.6 is synchronous. $\checkmark$
5.8 A synchronous counter uses a common clock pulse $\checkmark$ that tries to clock all the various JK flip-flops simultaneously.

An asynchronous counter has a clock pulse only on the first JK latch $\checkmark$ that serves as the LSB. The output of each JK latch is set as the clock pulse for the next JK latch.
5.9 - SIPO (Series-In-Parallel-Out) $\checkmark$

- PIPO (Parallel-In-Parallel-Out) $\checkmark$
- SISO (Series-In-Series-Out) $\checkmark$
- PISO (Parallel-In-Series-Out)
(Any $3 \times 1$ )
5.10

$$
\begin{aligned}
& \checkmark \text { Drawing correct } \\
& \checkmark \text { Q } 0 \text { to Q } 2=1 \text { Mark }
\end{aligned}
$$



MARKING NOTE:

- Each correct label receives 1 mark. NB: Q0 to Q2 together counts as one label.
- 1 mark is awarded for the sketch if it is correct.


## QUESTION 6: MICROCONTROLLERS

6.1 Microcontrollers are used in industrial control devices:

- Industrial instrumentation $\checkmark$
- Monitoring
- Process control
- Cooling systems

$$
\begin{equation*}
\text { (Any } 2 \times 1 \text { ) } \tag{2}
\end{equation*}
$$

6.2 NOTE:


NOTE: 1 mark for each correct label = 3
1 mark for correct connection and direction = 1
6.3 A microprocessor is simply an IC which has only the Central Processing Unit (CPU) inside it.
A microcontroller is essentially a complete, small-scale computer $\checkmark$ with all the necessary devices required to function, $\checkmark$ embedded together on a single IC chip.

## OR

A microprocessor is an Integrated Circuit (IC) with only a Central Processing Unit (CPU).
A microcontroller is a complete, small-scale computer with all the necessary devices required to function, embedded together on a single IC chip.
6.4


## NOTE:

- 1 mark is awarded per label.
- The 6th mark is awarded if the learner has all labels correct and the sketch is also drawn correctly.
- The positioning of the components may differ from that of the marking guideline.
6.5 - Memory data register $\checkmark$
- Memory address register $\checkmark$
- Counter register $\checkmark$
- Control register
- Current-instruction register
(Any $3 \times 1$ )
6.6 It receives analogue data $\checkmark$ and converts the data into digital format which is then made available to the microcontroller to do further calculations.
6.7

6.8 - Half-duplex $\checkmark$
- Full duplex/duplex $\checkmark$
6.9 6.9.1 The Memory Data Register (MDR) stores a copy $\checkmark$ of the current instruction to be executed.
6.9.2 The Current Instruction Register (CIR) splits the instruction into two parts. $\checkmark$ One part is decoded by the control unit ready for execution, $\checkmark$ the other part is the address of the data stored that needs to be used together with that instruction.
6.10 Takes up a lot of space on the logic chip for the parallel paths.

Makes manufacturing the logic chip more complex.
6.11 6.11.1 • Point of sale (POS) terminals $\checkmark$

- Metering instruments $\checkmark$
- Large special automated machines
- Modems
- Computer Numerically Controlled machines (CNC) Robots
- Embedded control computers
- Medical instruments and equipment
6.11.2 Differential $\checkmark$
6.12 In legal data flow the data lines will follow on another after a specific action $\checkmark$ and they will not cross one another or move against the other flow.

In illegal data flow the data lines will just end without reaching a function. The data lines may also move against the flow of other lines or the data lines may cross other data lines.
6.13


1 mark for each correctly placed label = 6
1 mark for each correctly placed flow line = 2

